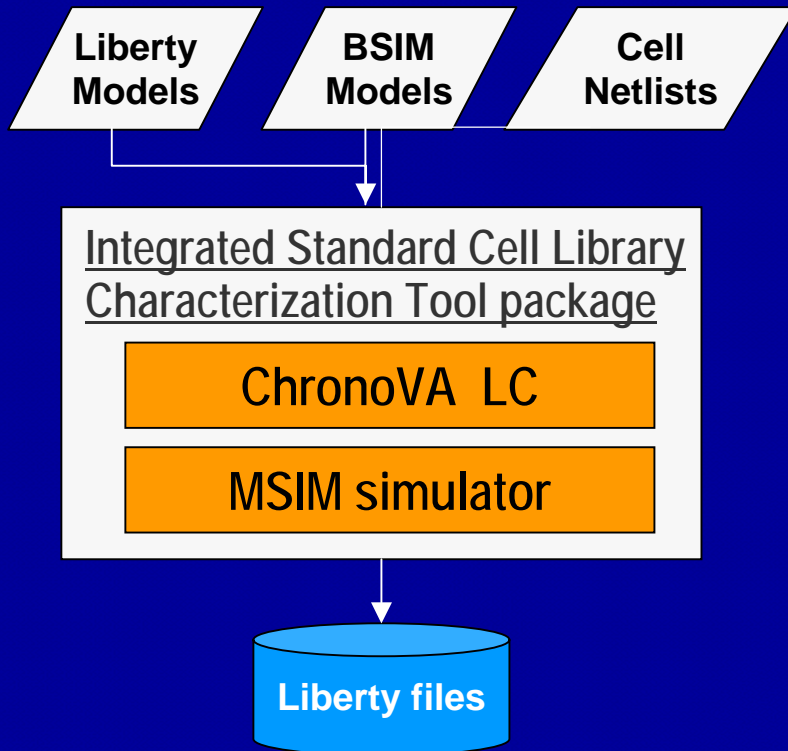


Cell Library Characterization

ChronoVA LC bundled with Volume MSIM



- ◆ Easy adopt by standard input
 - Liberty, BSIM, subckt
 - User friendly tcl scripts
- ◆ Multi-choices of
 - P.V.T. corners
- ◆ Support industrial models
 - ECSM, CCS, CCS Compact
 - Timing, power models
- ◆ Bundled with the volume MSIM circuit simulator

ChronoVA® is the Trademark of Anova Solutions, Inc.

Technology Leader in IP Characterization and IC/PCB Simulation

Legend
Design Technology

The Benefits

ChronoVA LC bundled with Volume MSIM

- ◆ Very short Turn-Around Time (TAT)
Cell library characterization can be done in hours, instead of days.
- ◆ High accuracy and high reliability
- ◆ Fully automated and easy to set up
- ◆ Cloud computing ready
- ◆ Best cost-performance

Enabling quick 'time to market'!

Benchmark Data

ChronoVA LC bundled with Volume MSIM

TSMC 40G standard cell library

- ◆ 1000 cells, 7x7 table size, 60 CPUs
 - ~8 hour TAT
- ◆ 1000 cells, 7x7 table size, 100 CPUs
 - ~6 hour TAT
- ◆ 1000 cells, 7x7 table size, 1000 CPUs
 - ~30 minutes

MSIM[®] Certifications

- ◆ MSIM certified by TSMC's Spice Tool Qualification Program

<http://www.legenddesign.com/BW/021009.shtml>

- ◆ JEITA (Japan Electric and Information Technology Industries Association) has qualified MSIM-PCB for simulating IBIS models in PCB designs. Those results can be downloaded from JEITA web:

<https://ec.jeita.or.jp/ibis/>