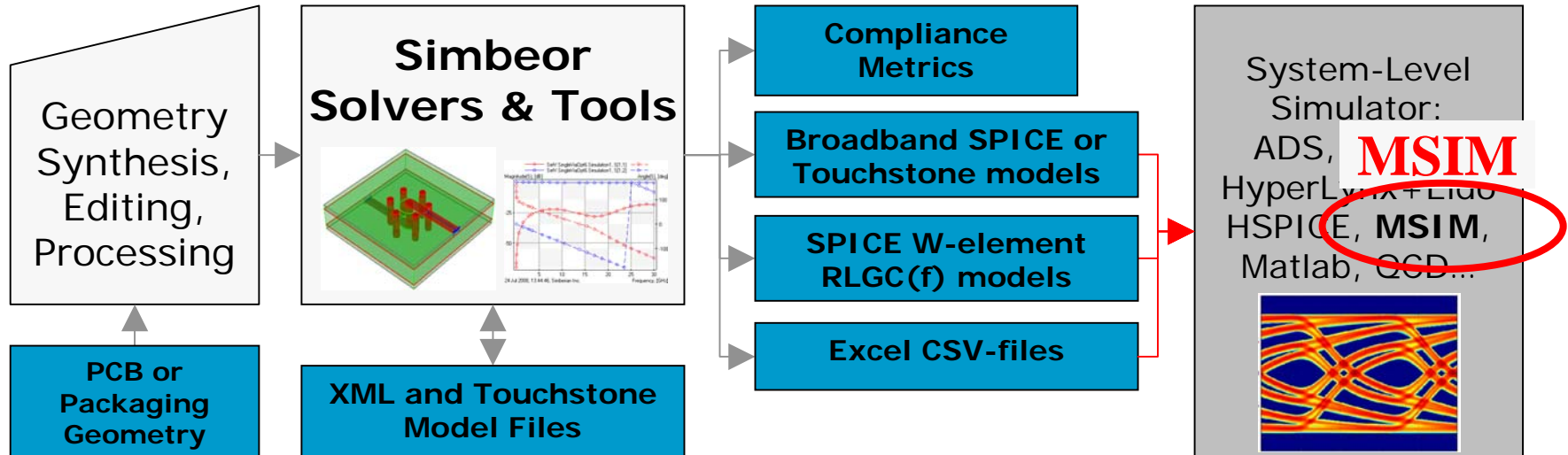


# Simbeor is synthesis, full-wave analysis and macro-modeling tool for interconnects

Simbeor enables **geometry synthesis** for controlled impedance transmission lines and via-holes, has **geometry import** and selection capabilities, and **3D geometry editor**

Simbeor 2012 can be used as a stand-alone tool to verify compliance metrics



Simbeor produces SPICE RLGC(f) models for transmission lines, broadband SPICE or Touchstone S-parameter models and time-domain responses for interconnect elements and for interconnect of a complete data link

Simbeor is one-stop solution for passive interconnect pre and post-layout analyses with advanced electromagnetic models, for macro-modeling and material parameters identification tasks

# MSIM<sup>®</sup>-PCB

## A Complete PCB Spice Simulator

- ◆ Intelligent algorithms of high speed simulation for high accuracy and excellent convergence.
- ◆ Extensive model support for W (T or U) element with RLGC, broadband (BB) Spice macro model, IBIS, and encrypted IO circuit model
- ◆ Built-in PRBS and Pulse generator with pre/de-emphasis, and jitter distortion etc.
- ◆ Built-in transmission line & W-element model extraction

# MSIM<sup>®</sup> Certifications

- ◆ JEITA (Japan Electric and Information Technology Industries Association) has qualified MSIM-PCB for simulating IBIS models in PCB designs. Those results can be downloaded from JEITA web:

*<https://ec.jeita.or.jp/ibis/>*

- ◆ MSIM certified by TSMC's Spice Tool Qualification Program

*<http://www.legenddesign.com/BW/021009.shtml>*