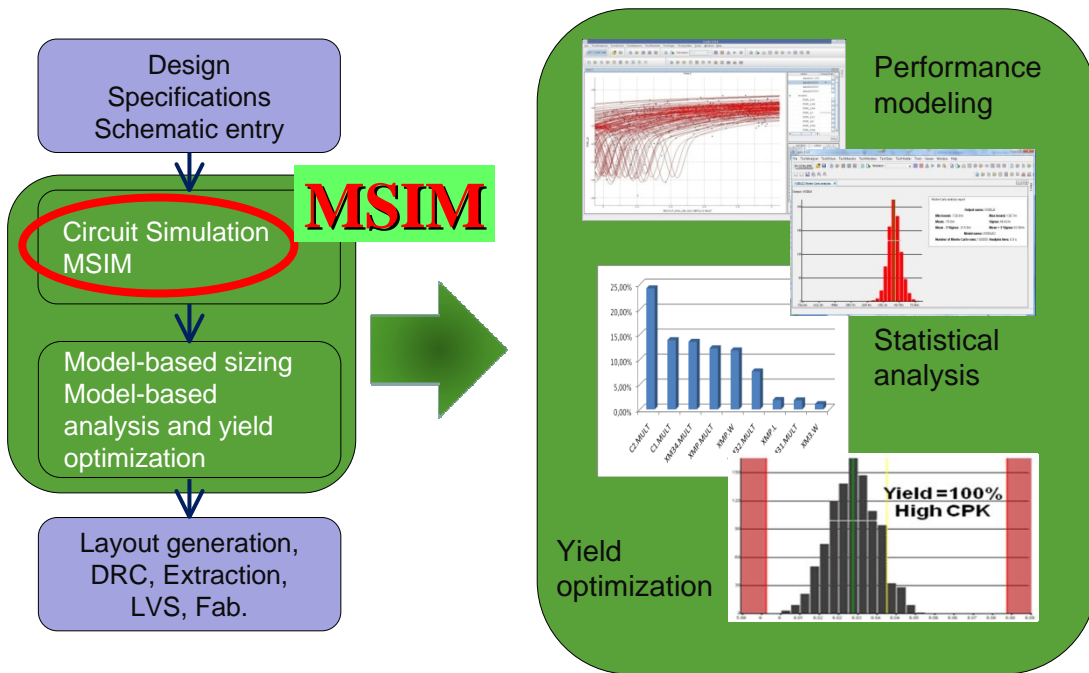


## Lysis and MSIM-based analog design flow

LYSIS™ is the world's unique model-based parametrical DFY solution. This innovative and unique technology automatically drives MSIM to simulate your design and provides the best design solutions for the most advanced (sub 100 nm) and complex designs. Different from traditional tools, LYSIS can explore the entire design space and find the best global yield and robustness for your design. It is the first yield explorer and optimizer.

No compromise has to be done on the yield estimation, LYSIS will let you exploring the design's yield space, improving design performance and getting the most reliable results using the power of the model-based technology.”



## Features and Benefits

- Handles statistical variations (Process, T°, Bat...)
- Finds the best design solution that would yield 100%
- Accurate yield analysis of the design in minutes
- Centers, reduces variance and raises CPK of a given design within hours
- Yield maximization, design centering, robustness optimization and statistical corners
- Manages Yield trade-offs by setting constraints to Lysis optimizer

## Typical Applications

- Large and complex analog, AMS and RF designs
- LDO, VCO, ADC, DAC, Biasing, Band gap...
- Filters, Current sources, latches, transceivers
- Cascode gain stage, SER/DES.....

# MSIM<sup>®</sup> Certifications

- ◆ MSIM certified by TSMC's Spice Tool Qualification Program

*<http://www.legenddesign.com/BW/021009.shtml>*

- ◆ JEITA (Japan Electric and Information Technology Industries Association) has qualified MSIM-PCB for simulating IBIS models in PCB designs. Those results can be downloaded from JEITA web:

*<https://ec.jeita.or.jp/ibis/>*